

501.38505CX2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: N. YAMAMOTO, et al.

Serial No.: 10/760,358

Filed: January 21, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
METHOD FOR MANUFACTURING THE SAME

Group: 2825 (previous)

Examiner: C.A. LUU (previous)

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 13, 2004

Sir:

In the matter of the above-identified application, it is requested that the Examiner consider the document listed on the attached form equivalent to Form PTO-1449. A copy of the document is being submitted herewith.

This information disclosure statement is being submitted within three months of the filing date.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of

Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Case:
501.38505CX2), and please credit any excess fees to such deposit account.

Respectfully submitted,

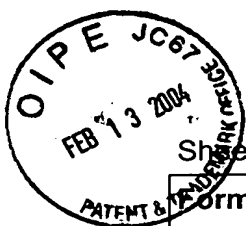
ANTONELLI, TERRY, STOUT & KRAUS, LLP



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Attachments



Sheet 1 of 1

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. 501.38505CX2	SERIAL NO. 10/760,358
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT YAMAMOTO et al.	
		FILING DATE January 21, 2004	GROUP

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
	6,593,229	07/15/03	Yamamoto, et al.			08/09/00

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation /Abstract	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner		Date Considered